

10039045



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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10039045	FILING DATE 12/31/2001	CLASS 711	SUBCLASS 152	GAU 2186	EXAMINER Li
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**APPLICANTS: Vo Hahn;

**CONTINUING DATA VERIFIED:

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** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		H052617.1142US0	
Verified and Acknowledged Examiners's initials			
TITLE : Low latency lock for multiprocessor computer system			
U.S. DEPT. OF COMM./PAT. & TM.-PTO-435L (Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
ISSUE FEE		Sheets Drawg.	Figs. Drawg.
Amount Due	Date Paid	Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
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